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Explanation: Very large scale integration is the process of creating an integrated circuit with thousands of transistors into ...

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Multiple Choice Questions and Answers on VLSI Design & Technology Multiple Choice Questions and Answers By Sasmita January 13, 2017 1) The utilization of CAD tools for drawing timing waveform diagram and transforming it into a network of logic gates is known as _____.

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Answer : As discussed in the above question, the V_t depends on the voltage connected to the Body terminal. It also depends on the temperature, the magnitude of V_t decreases by about 2mV for every 1 °C rise in temperature. VHDL Interview Questions . Question 10. What Are The Steps Required To Solve Setup And Hold Violations In Vlsi? Answer :

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Answer: a Explanation: Very large scale integration is the Vlsi Objective Questions With Answers This set of VLSI Multiple Choice Questions & Answers focuses on "Scan Design Techniques-2". 1. The serial shift register is driven using a) one over-lapping clock b) two over-lapping clock c) one non over-lapping clock d) two non over-lapping clock View Answer.

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Answer: a Explanation: Very large scale integration is the Vlsi Objective Questions With Answers This set of VLSI Multiple Choice Questions & Answers focuses on "Scan Design Techniques-2". 1. The serial shift register is driven using a) one over-lapping clock b) two over-lapping clock c) one non over-lapping clock d) two non over-lapping clock View Answer.

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10. Give the Cross-sectional diagram of the CMOS. VLSI VIVA Questions and Answers :

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1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

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